



976.1001US

UNITED STATES PATENT AND TRADEMARK OFFICE

Re: Application of: Stephen L. ABBOTT, et al.
Serial No.: 09/808,067 *#6*
Filed: March 14, 2001
For: **ECONOMIC SUPPLY OPTIMIZATION SYSTEM**

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

January 29, 2004

Sir:

In accordance with Applicant's duty of disclosure under 37 C.F.R. § 1.56 and the provisions of 37 C.F.R. §§ 1.97 and 1.98, Applicants hereby make of record the references listed on the attached Form PTO-1449.

Respectfully submitted,

DAVIDSON, DAVIDSON & KAPPEL, LLC

By:

Cary S. Kappel
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RECEIVED

FEB 04 2004

Technology Center 2100

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<p style="text-align: right;">O P T FEB 02 2004 JC 10 U.S. PATENT & TRADEMARK OFFICE</p> <p>LIST OF PRIOR ART CITED BY APPLICANT (Use several sheets if necessary)</p>										ATTY. DOCKET NO.: 976.1001US	SERIAL NO.: 09/808,067								
										APPLICANT(S): Stephen L. ABBOTT, et al.									
										FILING DATE: March 14, 2001					GROUP: 2122				
U.S. PATENT DOCUMENTS																			
*EXAMINER INITIAL		DOCUMENT NUMBER								DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE					
	AA	5	6	3	0	0	7	0	05/13/1997	Dietrich, et al.	395	208							
	AB	5	9	1	8	0	5	1	05/29/1999	Savitzky, et al.	395	683							
	AC	5	9	7	1	5	8	5	10/26/1999	Dangat, et al.	364	468.08							
	AD	6	0	6	1	7	5	0	05/09/2000	Beardsley, et al.	710	74	RECEIVED						
	AE	6	0	8	5	4	9	3	07/11/2000	DeBusk, et al.	53	445	FEB 04 2004						
	AF	6	1	4	1	7	0	8	10/31/2000	Tavallaei, et al.	710	62							
	AG	6	1	5	9	4	1	1	12/12/2000	Kulkarni, et al.	264	401	Technology Center 2100						
FOREIGN PATENT DOCUMENTS																			
		DOCUMENT NUMBER								DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION					
														YES	NO				
	AH																		
	AI																		
	AJ																		
OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)																			
	AK	Tang, Y., et al., "INTEGRATED APPROACH TO DISASSEMBLY PLANNING AND DEMANUFACTURING OPERATION," IEEE International Symposium on Electronics and the Environment, San Francisco, May 8-10, 2000, Proceedings of ISEE 2000, pp. 354-359																	
	AL	Mok, S.M. et al., "SYSTEM FOR ANALYZING AUTOMATIC ASSEMBLY AND DISASSEMBLY OPERATIONS," IEEE International Conference on Robotics and Automation, San Francisco, April 24-28, 2000, Symposia Proceedings, 2000, 2000 ICRA Millenium Conference, vol. 4 pp. 3695-3700.																	
	AM	Limaye, K. Et al., "SYSTEM SIMULATION AND MODELING OF ELECTRONICS DEMANUFACTURING FACILITIES," IEEE International Symposium on electronics and the Environment, Danvers, MA, May 11-13, 1999, Proceedings of ISEE 1999, pp. 238-243.																	
	AN	Srinivasan, H. et al., "FRAMEWRK FOR VIRTUAL DISASSEMBLY ANALYSIS," J. Of Intelligent Manufacturing, vol. 8, Issue 4, August 1997, pp. 277-295.																	
	AO	Kalyan Seshu, U.S., et al., "TOWARDS COMPUTER AIDED DESIGN FOR THE LIFE CYCLE," IEEE International Symposium on electronics and the Environment, Oak Brook, IL, May-4-6, 1998, pp. 31-315.																	
	AP	Tai-Yue Wang, et al., "MACHINE SELECTION IN FLEXIBLE MANUFACTURING CELL: A FUZZY MULTIPLE ATTRIBUTE DECISION-MAKING APPROACH," International J. Of Production Research, vol. 38, Issue 9, June 15, 2000, pp-2079-2097.																	
EXAMINER										DATE CONSIDERED									
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>																			